Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claims 1.-21. (Canceled)

22. (New) A power on/off reset circuit comprising:

a first voltage detection circuit which detects a first voltage and outputs a first signal which is transmitted as the detected first voltage,

a second voltage detection circuit which detects a second voltage higher than the first voltage and outputs a second signal which is transmitted as the detected second voltage,

a third voltage detection circuit which detects the first voltage and the second voltage and outputs a third signal which is transmitted as the detected first voltage and the detected second voltage, and

a first circuit which has a first function of executing a series of operational sequences in accordance with an input control signal and a second function of not accepting said input control signal,

wherein the third signal is transmitted as the second voltage when a power-supply voltage rises, and transmitted as the first voltage when the power-supply voltage drops, and

the first function of the first circuit continues an ongoing sequence according to the input control signal and the second function of said first circuit prevents a new operational sequence regardless of the input control signal when a power-supply voltage is equal to or lower than a voltage for the third signal transmitted.

23. (New) The power on/off reset circuit according to claim 22 further comprising:

a fourth voltage detection circuit which detects a third voltage lower than the first voltage and outputs a fourth signal which is transmitted as the detected third voltage,

a second circuit which executes a series of operational sequences in accordance with the input control signal,

wherein an operation, which is being executed by the fourth voltage detection circuit, is immediately suspended when the power-supply voltage is equal to or lower than the third voltage.

24. (New) The power on/off reset circuit according to claim 23,

wherein a time for the power-supply voltage to drop from the first voltage to the third voltage is longer than a predetermined operational sequence completion time.

25. (New) A semiconductor device comprising:

the power on/off reset circuit of claim 22, 23 or 24, and a non-volatile memory, wherein

the first circuit of the power on/off reset circuit controls said non-volatile memory.